

«CaseNumber»

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Lo, William

Group Art Unit: 2667

Serial No.: 09/583316

Examiner: A. Qureshi

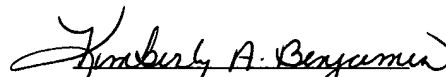
Filed: May 30, 2000

Title: Reduced Pin Gigabit Media Independent Interface

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, Alexandria, VA 22313-1450 on this date.

Date: March 22, 2005


Kimberly A. Benjamin

SUBMISSION OF FORMAL DRAWINGS

MAIL STOP ISSUE FEE

Commissioner for Patents

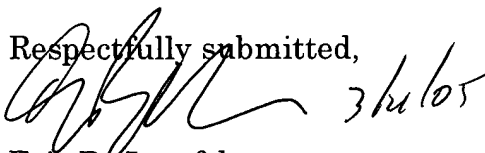
P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Enclosed is a set of formal drawings which are submitted in response to the Notice of Allowability dated December 29, 2004, consisting of 5 replacement sheets. Please note that no changes have been made. The drawings have been merely formalized. Please enter these drawings in the application so that it may pass to issue.

Respectfully submitted,



Eric B. Janofsky

Attorney for Applicant

Registration No. 30759

Please address all correspondence to:

Marvell Semiconductor, Inc.

645 Almanor Avenue

Sunnyvale, CA 94085

Phone: (408) 222-1953

Fax: (408) 328-0918

Date: 3/24/05



Application No. 09/583,316
Attorney's Docket No. MP0050
Page 1

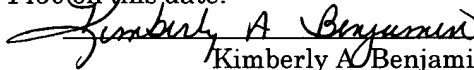
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	Examiner: Afsar M. QURESHI
William LO)	
)	Group Art Unit: 2667
Application No.: 09/583,316)	
)	Confirmation No.: 1557
Filed: May 30, 2000)	
)	
For: REDUCED PIN GIGABIT MEDIA)	Date: March 29, 2005
INDEPENDENT INTERFACE)	

CERTIFICATE OF MAILING

I hereby certify that this correspondence, and the documents attached hereto, are being deposited with the United States Postal Service as First Class mail in an envelope addressed to Commissioner for Patents, Alexandria, VA 22313-1450 on this date.

Date: March 22, 2005


Kimberly A Benjamin

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant thanks the Examiner for the Notice of Allowance and Allowability mailed December 29, 2004, indicating that claims 1-48 are allowed. The issue fee is being paid concurrently herewith. Applicant respectfully submits the comments below to clarify for the record the Examiner's Statement of Reasons for Allowance.

The Examiner has stated that

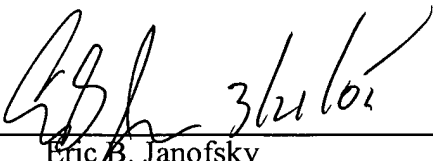
[t]he present invention relates to forming a reduced pin media independent interface. The unique method steps including forming encoding and decoding a transmit control signal comprising "a transmit enable signal in one half of a cycle

of the clock signal and a transmit error signal in the other half (claims 1, 20, 28, 47, 55, 69, 82, 95, 96, 114, 119, 129, 134, 139 and 140), also, forming encoding and decoding a receive control signal comprising in one half of a cycle of the clock signal a receive data valid signal or a carrier sense signal, and in other half of the cycle of the clock signal, a receiver error signal" (claims 6, 15, 33, 42, 60, 74, 87, 109 and 124). The closest prior art, Runaldue et al. (US 6,108,726) and RMII Specification Rev. 1.2 (pp 1-14) are in the same field of endeavor but fail to disclose the above underlined unique method steps and render the claims allowable. [Notice of Allowability, page 2 (emphasis in original)]

While at least some of the claims of the present application are allowable for the reasons specified by the Examiner, Applicant respectfully notes that each claim must be analyzed for patentability on its own merits. Accordingly, Applicant respectfully submits that each of claims 1-48 is patentable over the prior art of record for at least the reasons presented by the Examiner and previously by the Applicant during the prosecution of the present application.

Should the Examiner have any questions regarding these comments or the application in general, the Examiner is urged to contact the Applicant's attorney, Eric B. Janofsky, by telephone at (408) 222-9071. All correspondence should continue to be directed to the address given below.

Respectfully submitted,

By: 
Eric B. Janofsky
Attorney for Applicant
Registration No. 30,759

Marvell Semiconductor Inc.
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MP0050

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Lo, William

Serial No.: 09/583316

Filed: May 30, 2000



Group Art Unit: 2667

Examiner: A. Qureshi

Title: Reduced Pin Gigabit Media Independent Interface

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Date: *March 22, 2005*

Kimberly A. Benjamin
Kimberly A. Benjamin

COMMUNICATION TO UPDATE CUSTOMER NUMBER

MAIL STOP ISSUE FEE

Commissioner For Patents

PO Box 1450

Alexandria, VA 22313-1450

Sir:

Applicant requests that the U.S. Patent and Trademark Office update the Customer Number in its records in the above-identified application to:

28285

Respectfully submitted,

Eric B. Janofsky

Attorney for Applicant

Registration No. 30759

Date: